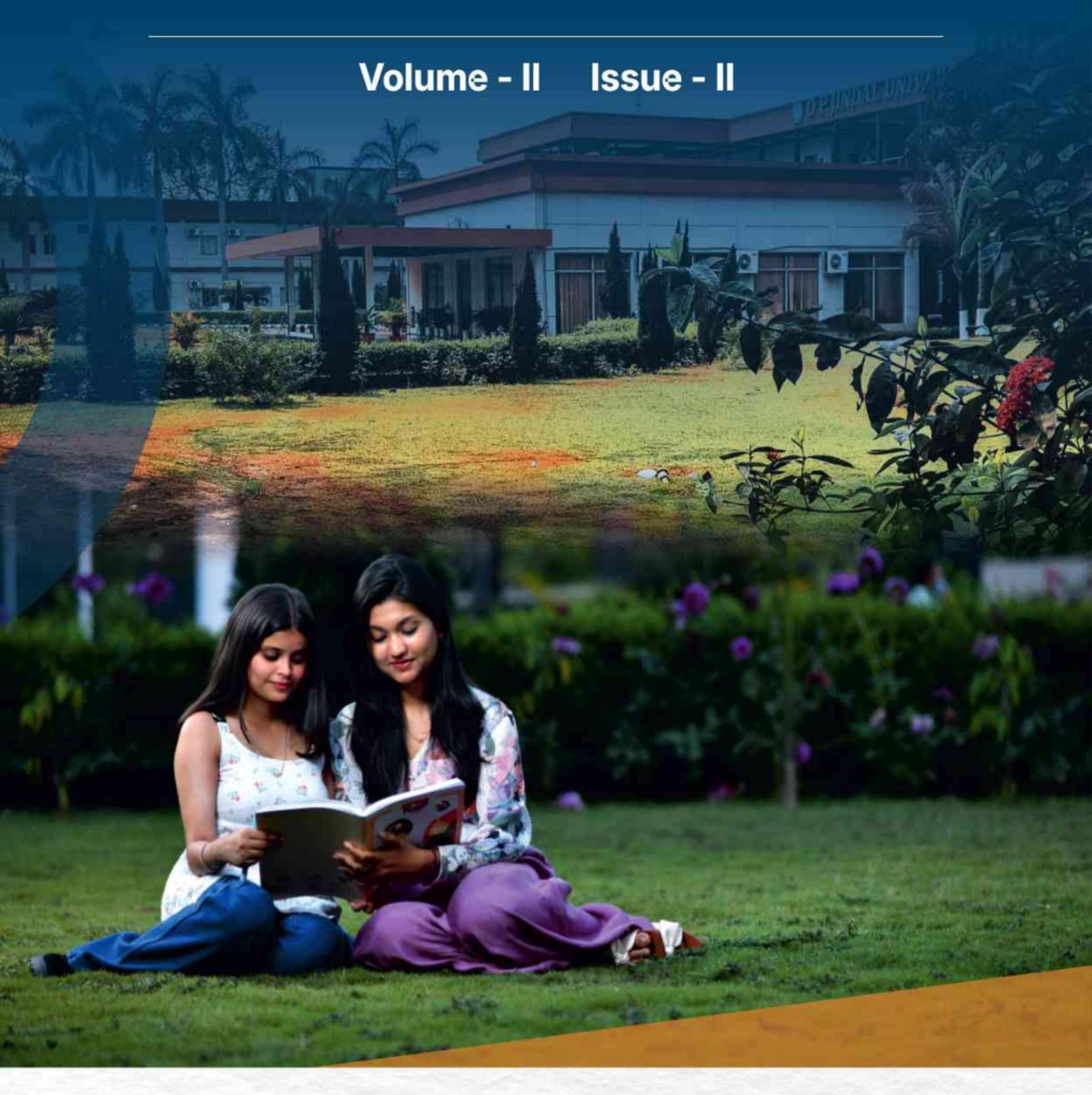
NEWSLETTER 2025

DEPARTMENT OF ELECTRICAL ENGINEERING







Message by the Vice Chancellor

It is with immense pride and excitement that I welcome you to the latest edition of our Electrical Engineering Department's newsletter. For years, our department has been a beacon of academic brilliance, fostering innovation, and shaping the future of electrical engineering. The field of Electrical Engineering stands at the heart of addressing the dynamic challenges of today's world—from spearheading sustainable energy solutions to pioneering advancements in electric vehicles. Our department has continually led the way in tackling these crucial societal issues.

Our faculty, researchers, and students are at the forefront of pushing the limits of knowledge, contributing to progressive research and state-of-the-art projects. I take immense pride in their dedication and hard work. This year has been particularly remarkable as our university OPJU is accredited with Grade A by NAAC (National Assessment and Accreditation Council). It is a pride for OPJU for being the first private university in the state of Chhattisgarh and in central India to receive an "A" grade in the first cycle of the NAAC accreditation process. This is a testament to our unwavering commitment to excellence in higher education.

In line with the evolving needs of education, our university is aligning its curriculum with the progressive National Education Policy (NEP), ensuring we stay ahead in the ever-changing educational landscape.

As we move forward, I encourage all of you to aim for excellence, collaborate across disciplines, and embrace cutting-edge technologies that will shape the future of electrical engineering. Together, we can tackle the pressing challenges of our time.

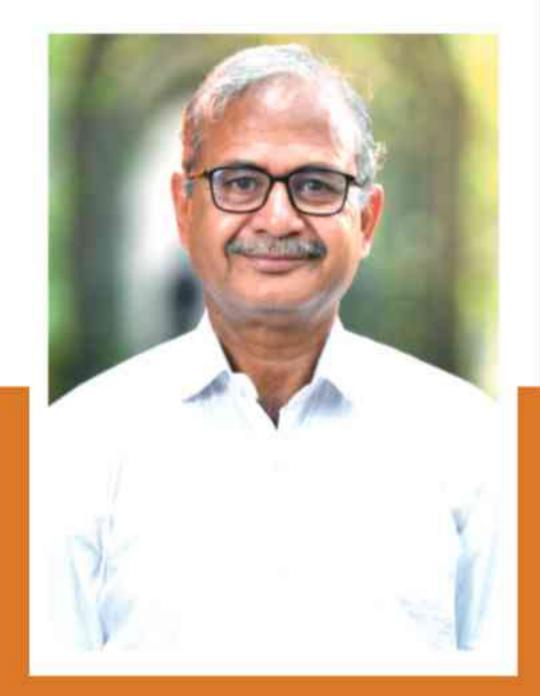
I am excited to watch our Electrical Engineering Department thrive and achieve even greater success. Let's continue to work together to inspire, innovate, and make a lasting impact on the world. Thank you for your unwavering commitment to the highest standards of electrical engineering.

Dr. R. D. Patidar

Message by the Vice Dean

Dr. R. S. TARE

Vice Dean- Electrical Engineering O.P. Jindal University, Raigarh(C.G.)



It is my honor and privilege to welcome you to the 2nd edition of "SPARK", the biannual newsletter of the Department of Electrical Engineering. As Vice-Dean, I take great pride in the achievements of our students, faculty, and researchers who continue to push the boundaries of innovation and excellence.

Electrical Engineering lies at the heart of the technological revolution shaping our modern world. From power systems and telecommunications to embedded systems and artificial intelligence, our field is critical in solving real-world challenges and enabling sustainable development.

Our department is committed to providing a dynamic and forward-thinking educational experience. We emphasize not only theoretical understanding but also practical application through cutting-edge laboratories, industry collaborations, and research initiatives. We continually update our curriculum to reflect emerging technologies and the demands of a rapidly evolving global market.

To our students: You are the future engineers, thinkers, and leaders. I encourage you to explore boldly, question thoughtfully, and contribute meaningfully. The opportunities here are vast—make the most of them.

To our alumni and industry partners: Thank you for your continued support and engagement. Your successes inspire our current students and strengthen the legacy of this department. Let us continue to work together to drive progress and create a positive impact through innovation, ethics, and excellence in electrical engineering.

Message by the Head of Department

Dr. S. Diptimayee Swain

Head- Electrical Engineering O.P. Jindal University, Raigarh(C.G.)



It is with great excitement that I introduce the 2nd edition of "SPARK", the biannual newsletter of the Department of Electrical Engineering our flagship program that continues to be highly sought after by students. I would like to extend my heartfelt greetings to our esteemed faculty members, whose relentless pursuit of knowledge and dedication to excellence continue to drive progress in this ever-evolving field. Our department remains steadfast in its commitment to providing a dynamic learning environment that fosters creativity, nurtures innovation, and promotes critical thinking.

Our department places a strong emphasis on both research and innovation, complementing our unwavering dedication to teaching. Our faculty members are at the forefront of pioneering research across a variety of ground breaking fields, including Electric Vehicles, Photovoltaic Cells, Artificial Intelligence, Robust Control System Design with IoT and Python, and Embedded Systems. Their unwavering commitment to excellence in Electrical Engineering has played a pivotal role in establishing our department as one of the premier research hubs in the discipline.

With the collective dedication of our esteemed faculty and the exceptional talent of our students, we are spearheading ground-breaking research and innovation. We deeply value your continued support and encouragement, which empower us to push the boundaries of knowledge and shape the future of technology. Together, we will continue to make significant advancements and lead the way in these vital fields. Thank you for your ongoing support.

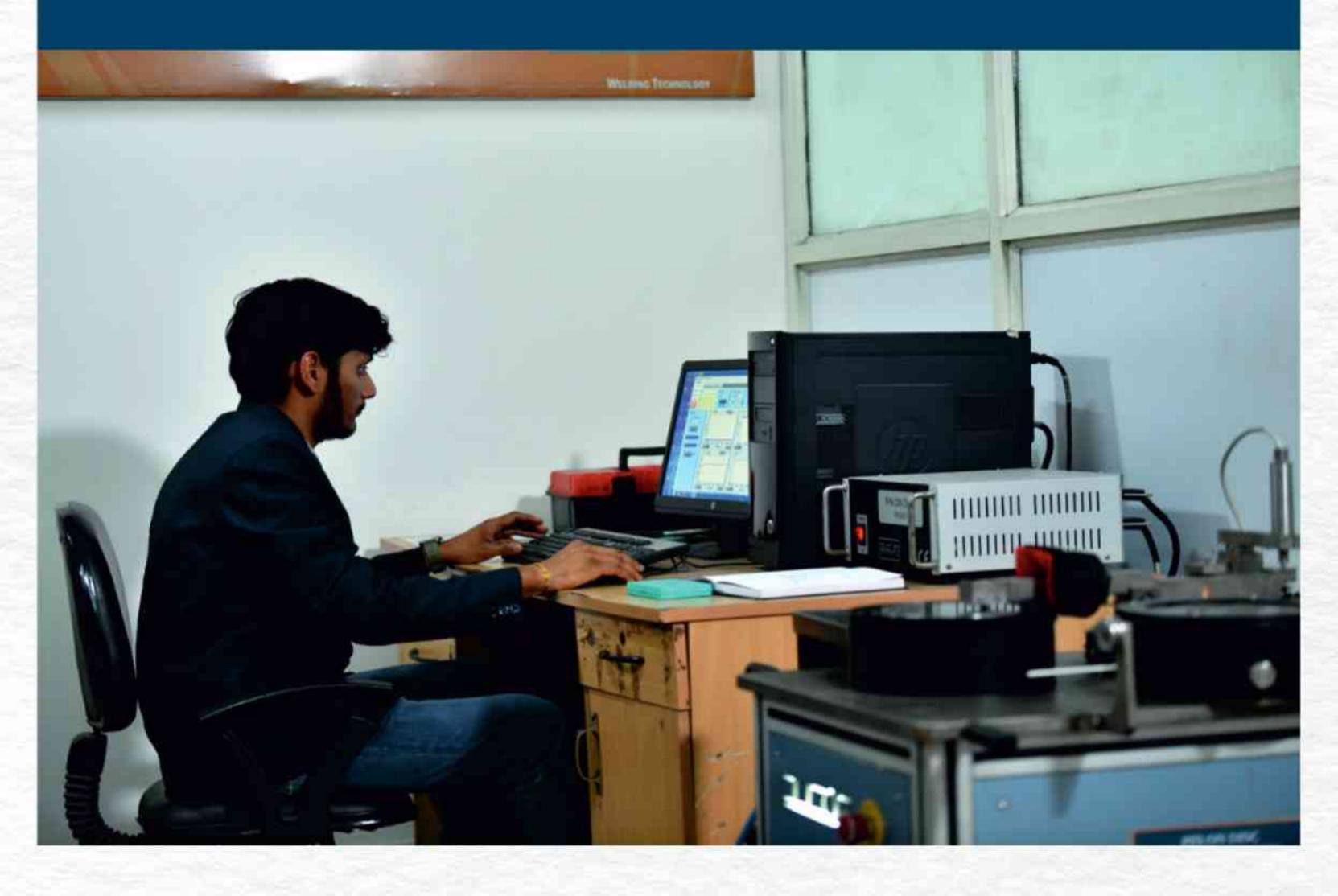
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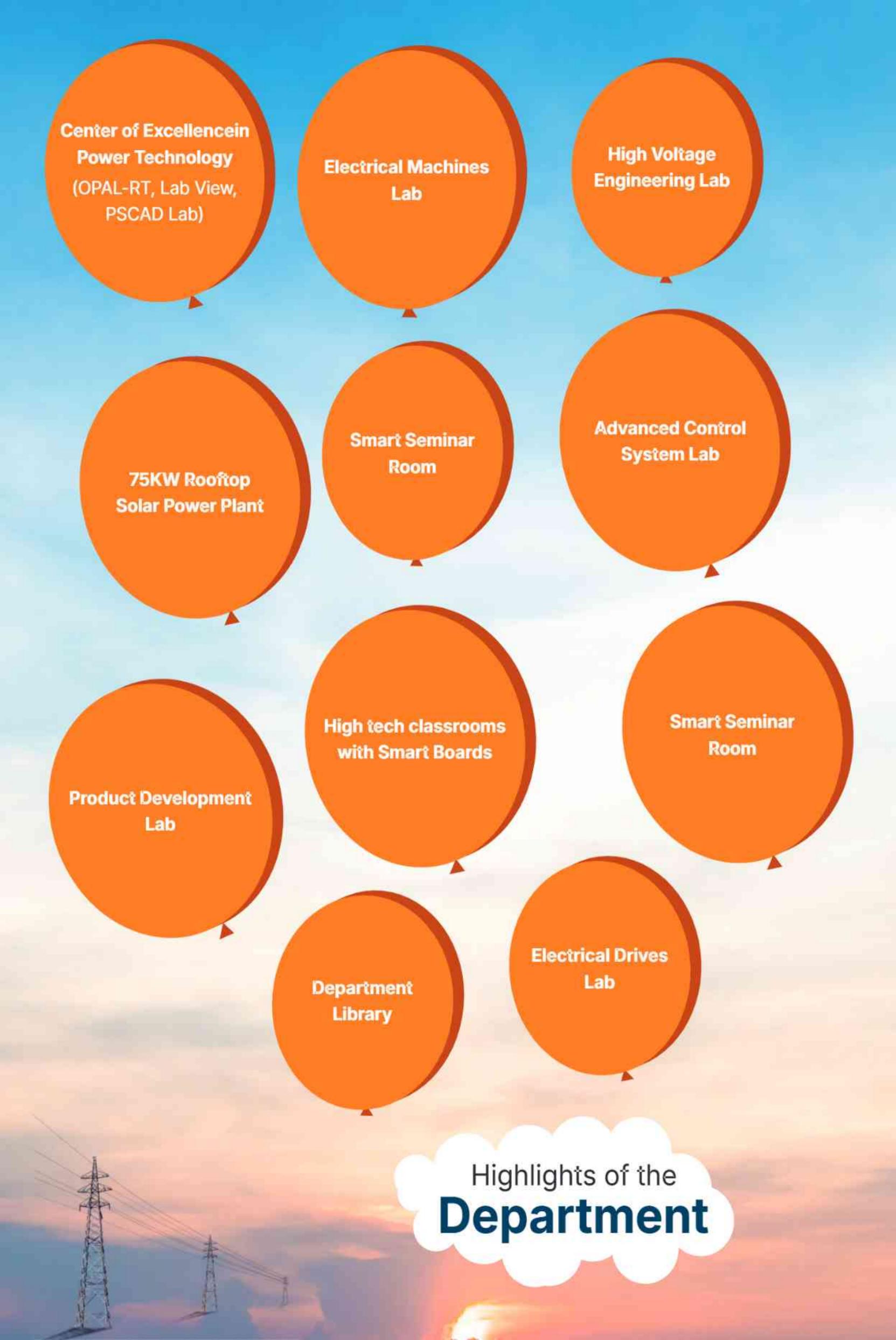
Department of Electrical Engineering

The Department of Electrical Engineering at O. P. Jindal University is dedicated to shaping graduates into skilled professionals and responsible citizens who will emerge as effective leaders and trailblazing innovators in the rapidly evolving fields of Electrical Engineering. We are committed to instilling in our students a strong sense of ethical values, along with a deep awareness of environmental and social issues.

Our department offers a range of educational programs, including Diploma, B. Tech, M. Tech, and Ph.D Initially, the B. Tech program in Electrical and Electronics Engineering was launched in 2008 at the O.P. Jindal Institute of Technology (OPJIT), affiliated with Chhattisgarh Swami Vivekananda Technical University (CSVTU). In 2014, OPJIT was granted university status (approved by UGC and AICTE) and was renamed O.P. Jindal University (OPJU). The same year, the M. Tech program in Power Electronics and Power Systems was introduced, followed by the initiation of the Ph.D program in 2017 to further enhance research opportunities. As a result, the department evolved into the Electrical Engineering Department.

To keep pace with advancements in technology, our curriculum is regularly updated, and our students have access to 22 state-of-the-art laboratories. These labs are equipped with cutting-edge technology and are supported by advanced experimental and computational resources, which facilitate research and consultancy projects across various domains of Electrical Engineering.

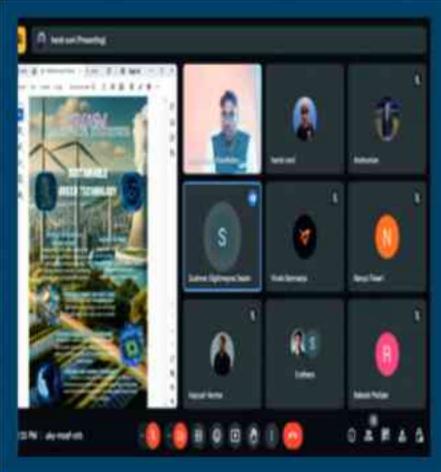




EVENTS & ACTIVITIES

On July 8, 2024, Mr. Rakesh Patidar and Mr. Shashikant Kaushalay organized a national-level digital poster competition focused on the theme "Recent Trends & Advancements in Electrical Engineering." This event aimed to provide a platform for students across OPJU to showcase their innovative ideas and research in the evolving field of electrical engineering





The total number of students registered was more than 50, of which 15 posters were shortlisted for online presentation. The winners of competition were-

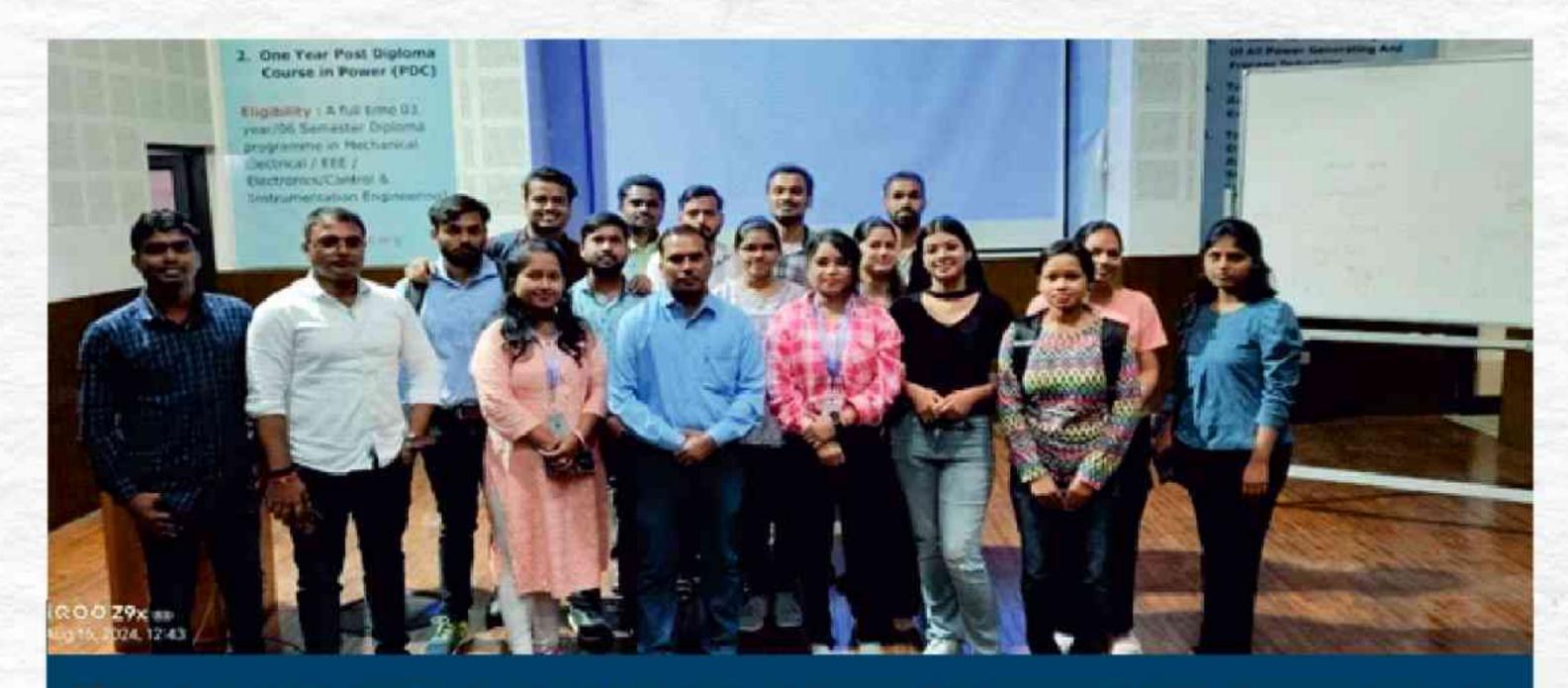
Navya Tiwari (I Position)

Ayush Verma (II Position)

Harsh Soni (III Position)

Bhavesh Prasad (III Position)

- On 14th August 2024, Prof. Pushpanjali Shadangi, Assistant Professor (Sr. Grade) in the Department of Electrical Engineering at OP Jindal University, delivered an insightful expert lecture on "Basics of Control Systems and Their Applications" at the Jindal Institute of Power Technology (JIPT), Tamnar.
- On 14th August 2024, Prof. Shashikant Kaushaley, Assistant Professor at OP Jindal University, delivered an insightful expert lecture on the "Basics of Synchronous Machines" at the Jindal Institute of Power Technology (JIPT), Tamnar. This session was part of the inaugural activities for the new batch of Post Graduate Diploma (PGDC) and Post Diploma (PDC) students for the academic year 2024-25.



On August 16, 2024, the Jindal Institute of Power Technology (JIPT), Tamnar, hosted a distinguished session on "Instrumentation & Measurement", delivered by the esteemed Prof. Rakesh Patidar. This lecture was a part of JIPT's ongoing commitment to fostering excellence in the power sector through knowledge sharing and continuous learning.

- On August 21st and 29th, 2024, the Jindal Institute of Power Technology (JIPT), Tamnar, hosted an enlightening two-part lecture series on "Basics of Theorems (DC Circuits), 1φ and 3φ AC Circuits", led by the esteemed Prof. Rupesh Patel.
- On September 5, 2024, the Department of Electrical Engineering commemorated Teachers' Day in honor of Dr. Sarvepalli Radhakrishnan, the esteemed philosopher and educator who served as India's second President. His birth anniversary is celebrated as Teachers' Day to recognize the invaluable contributions of educators to society. Some glimpses are herewith.







Department of Electrical Engineering celebrated "Engineers Day" on the birth anniversary of M. Visvesvaraya on 16th September 2024 and Organized a Technical Idea Presentation, Digital Poster Presentation, and Project Model Presentation. In Technical Idea, a total of 14 teams, and in Digital Posters 11 teams participated. The project Model presentation event will be held after the MST Examination. Mr. Rakesh Patidar (Assistant Professor) and Mr. Shashikant Kaushaley (Assistant Professor) coordinated the program well.









The winners of the Technical Idea Presentation are:

- 1) Mr. Vivek Kumar Bhagat (V Sem, CSE)
- 2) Ms. Navya Tiwari (V Sem, CSE)
- 3) Ms. Ambika Mishra (V Sem, EE)

The winners of Digital Poster Presentation are:

- 1) Mr. Navjeevan K. Mehta (V Sem, CSE)
- 2) Mr. Vivek Kumar Bhagat (V Sem, CSE)
- 3) Mr. Harsh Soni (V Sem, EE)

The Department of Electrical Engineering celebrated "Vishwakarma Puja" on 17th September 2024. The glimpses of the event are given below:





Department of Electrical Engineering, SOE has organized a technical event on occasion of "Engineers Day", which included technical ideal presentations, digital poster presentations, and project model presentations. More than 50 students have participated from all the



OPJU O. P. Jindal University, Raigarh



Celebrating the birth anniversary of

Mokshgundam Visvesvaraya

Engineer's Day

15 September 2024

On this occasion

Department of Electrical Engineering.

School of Engineering organizing the following events:-

Technical Idea Presentation > 16th Sep 2024

Digital Poster Presentation > 16th Sep 2024

Project Model Presentation > 30th Sep 2024



Scan This QR For Registration

Registration Link: https://forms.gle/pDvhT7NtVd5TNhBJ7

For more Detail:

Prof. Rakesh Patidar (Asst.Prof.)

Prof. Shashikant Kaushaley (Asst.Prof.)

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Co- Coordinator
Mr. Kapel Nashine Mr. Jugal Kishere
(9109977019) (7898647209)
Student Coordinator

Subhash Nayak Anisha Bharti (6268125773) (9693393718

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- On 15th October 2024, the Department of Electrical Engineering celebrated Engineers' Day by organizing a Project Model Competition, honoring the legacy of Sir M. Visvesvaraya, whose contributions to engineering continue to inspire generations.
- III Students from various engineering disciplines presented 12 innovative hardware projects, demonstrating their creativity and technical prowess. The event fostered collaboration among students from different engineering fields, highlighting the interdisciplinary nature of modern engineering challenges.
- 12 The program coordinators, Mr. Rakesh Patidar and Mr. Shashikant Kaushaley, Assistant Professors in the Department of Electrical Engineering, expressed their gratitude to the Dean of the School of Engineering, esteemed judges, and faculty members for their presence and support. Their encouragement and guidance were instrumental in motivating the participants and ensuring the success of the event.



The Department of Electrical Engineering has conducted an Electrical Vehicle Workshop and Quiz in the SPARDHAA'24 event on 16th and 17th Nov 2024, Organized by OP Jindal University. The session was led by Mr. Shashikant Kaushaley.







- The International Conference on Sustainable Power and Energy (ICSPE 2024) was successfully held by the Department of Electrical Engineering at O.P. Jindal University (OPJU) in a hybrid format on November 28–29, 2024, at the Jindal Centre, JSP Raigarh. Important topics covered at the conference included microgrids, sustainability, lowering carbon emissions, and using AI and machine learning in power electronics and systems. 78 of the more than 263 papers that were submitted were accepted.
- Chief Guest Prof. (Dr.) Ashok Kumar Triparty, former Director General of CPRI Bengaluru and Director of SIT Bhubaneswar, opened the conference and underlined the significance of implementing sustainable energy practices in order to slow down climate change. Dr. K.B. Mohanty, the Head of Department and Professor at NIT Rourkela, Dr. Subhojit Ghosh, the Professor, NIT Raipur), and Mr. Sabyasachi Bandopadhaya (Executive Director, JSP Raigarh).
- On December 14, 2024, O. P. Jindal University's Department of Electrical Engineering, in collaboration with the Institution's Innovation Council, organised National Energy Conservation Day. The distinguished Chief Guests, Dr. Rakesh Nayak, Director of IIC, OPJU, and Dr. Sanjay Singh Parihar, Professor in the Humanities Department, OPJU, opened the program at the beginning of the day. Expert speakers Mr. Ajan Saha, East Zone Manager of Fluke Instruments, and Mr. Neeraj Bansal, Central Area Manager, both spoke at the event. They gave a thought-provoking discussion on "Energy Conservation in Industries with Preventive Maintenance" in a hybrid mode. Participants can demonstrate their abilities in a group discussion, an open-ended e-quiz, and a poster presentation. The program was organised and the winners' rewards were given out by Dr. Deeptimayee Swain, an associate professor of EE, and Mr. Rakesh Patidar, an assistant professor of EE.



FOR WORKSHOP/WEBINAR/TALK CONDUCTED AT THE UNIVERSITY

Prom October 16–18, 2024, a three-day workshop on "Innovative Approach of Learning Power Electronics With Arduino" was organised by the Electrical Engineering Department of the School of Engineering, OPJU, in collaboration with IEEE MP Section, IIC OPJU, and IEEE PELS Society MP Section. Prof. (Dr.) R. S. Tare, retired professor of SGSITS Indore, was the speaker for the workshop, webinar, and talk.



On December 2, 2024, the EE, SoE department hosted a workshop on "How to make educational videos from PPT." The event's coordinator and moderator was Mr. Rupesh Patel, and the spea.



RESEARCH & PUBLICATIONS

- The Wiley Online book Linear and Nonlinear System Modelling includes a chapter titled "Power Quality Improvement by Using PV-Integrated DSTATCOM," written by Professor Pushpanjali Shadangi and Dr. Sushree Deeptimayee Swain.

 ISBN:9781119847533 | DOI:10.1002/9781119847533.
- A study titled "Impact of Natural Dust Deposition on the Efficiency of Solar Modules Through Field Testing and Modelling" was published in IEEE Xplore by Dr. Ankireddy Narendra of the Electrical Engineering department of the School of Engineering. www.https://ieeexplore.ieee.org/document/10797515.
- On July 19–21, 2024, Pushpanjali Shadangi, Sushree Deeptimayee Swain, and R.D. Patidar delivered a paper titled "Comparative Study of DSTATCOM Pulse Generation Techniques Performance" at the IEEE "Smart Power Control and Renewable Energy" conference on (ICSPCER-2024), which was hosted by NIT Rourkela.

"Analysis of Direct and Indirect Current Controller Techniques in P-Q theory based Shunt Active Power Filter" conference on "Smart Power Control and Renewable Energy" (ICSPCER-2024), which was hosted by NIT Rourkela from July 19–21, 2024, by Jitendra Kumar Sao, R D Patidar, and Sushree Diptimayee Swain.



ACHIEVEMENTS

In acknowledgement of her exceptional work as a reviewer at the IEEE conference on "Smart Power Control and Renewable Energy" (ICSPCER-2024), which was hosted by NIT Rourkela from July 19–21, 2024, Pushpanjali Shadangi was awarded a certificate of appreciation.



- From July 19–21, 2024, Sushree Diptimayee Swain reviewed papers for the IEEE conference on "Smart Power Control and Renewable Energy" (ICSPCER-2024), which was hosted by NIT Rourkela.
- Pushpanjali Shadangi participated in a one-week workshop on "Electric Vehicle and its Key Components" [EVIKC-2024], which was arranged by the Department of Electrical Engineering in collaboration with IEEE BIT-Sindri Student Chapter, BIT Sindri, and the Institution's Innovation Council, from July 19–24, 2024.



Ms. Anjali Yadav, PhD Scholar in the Electrical Engineering Department at the School of Engineering, participated in a five-day workshop on "Sustainable Development: Intersection of Green Energy and eMobility" (IGEEM-2024), which was sponsored by the Science and Engineering Research Board (SERB) and organised by the Department of Electrical Engineering at the National Institute of Technology Rourkela, Odisha, from October 16th –20th, 2024.



From October 25 to October 29, 2024, Ms. Anjali Yadav, a PhD scholar in the Electrical Engineering department at the School of Engineering, participated in a five-day FDP on "Advanced Power Electronics and Intelligent Optimisation Techniques For Electric Vehicles," which was organised by the Electrical and Electronics Engineering department at Avanthi Institute of Engineering and Technology (Autonomous) Vizianagaram.

PLACEMENTS AND INTERNSHIPS PLACEMENTS





INTERNSHIPS



Faculty Members

Dr. R. D. Patidar is a graduate in Electrical Engineering from GEC, Rewa, post graduate in Power Electronics with Gold Medal by SGSITS, Indore and Doctorate in Electrical Engineering with specialization in Electrical Power Quality Management from IIT, Roorkee. Presently he is working as Vice-Chancellor with OP Jindal University (OPJU), University of Steel Technology and Management, formally OP Jindal Institute of Technology (OPJIT), Raigarh (C.G.). Prior to Vice-Chancellor, He worked as Director of OPJIT, Dean School of Engineering, Registrar & Pro Vice-Chancellor of OPJU. He also worked as a lecturer to the professor, Dean (Academics) & In-charge Director with Mandsaur Institute of Technology, Mandsaur, from March 1999



to December 2012, where he managed a UNDPITDC/TRC/ Govt. of India project as Principal Investigator for the employment of rural people and setup a Technology Resource Centre at Manasa District Neemuch, (MP), India. He has also been associated with time to time in different capacities with central statutory / regulatory/ accreditation bodies of higher education i.e., AICTE, UGC, DST, NAAC, etc. He published/Presented more than 40 research papers in reputed International Journals/Conference Proceedings. His research interest includes Power Quality, Active Power Filters/STATCOM, and Renewable Energy Sources. He is having more than 25 years of academic, industry, research, and academic administration work experience.

Dr. R S Tare, Professor in Electrical Engineering Department has over 40+ years of teaching experience in different institutes of repute. He has obtained Batchelor of Engineering for Indore University in Electrical Engineering with Honours. Subsequently he obtained ME Electrical Engineering from Devi Ahilya Vishwavidyalay Indore. He earned is PhD in Electrical Engineering specialising in Power Systems from prestigious Indian Institute of Technology, Delhi. He has published eleven research papers in national and international Journals of repute. His research interests include Power System studies and applications of microcontrollers in Power Electronics. He has rich academic and industrial



experience. Many industries sponsored consultancies have been executed successfully under his supervision.

Dr. Sushree Diptimayee Swain has received her Ph.D degree from National Institute of Technology. Rourkela, India and M.Tech from BRM University Chennai. She received her B.Tech degree from Biju Patnaik University of Technology, Orissa, India. She has 12 yrs of experience in teaching as well as in research. Dr. Dipti is currently working as Senior Assistant Professor, School of Engineering at OP Jindal University, Raigarh. As a teacher and researcher, she is a member of professional bodies like IEEE society. Presently, he is Editorial Board Member & Review various National & International Journals of repute. She has experience of being Resource Person for various Workshops & Research Conferences. She has published



several Research Papers in International Journal, some book chapters in Elsevier, Willey and CRC press and presented his Research work in National and international Conferences. She expertise in power electronics application in power system, renewable energies, control system etc.

Dr. Deepak Singh has been awarded a PhD degree in Electronics & Tele Communication Engineering from National Institute of Technology, Rourkela in 2017. He has completed M.Tech degree in Electronics & Telecommunication Engg from Veermata Jijabai Technological Institute Mumbai, University of Mumbai in 2007, and bachelor in engineering (B.E) in Electronics and Instrumentation Engineering from Samrat Ashok Technological Institute, Vidisha, RGPV Bhopal, 2002. He has more than 13 years of teaching experience and more than 2.5 years of industry experience. He has published a number of research papers for journals and conferences and guided graduate and postgraduate students for



their research work. His current interest for research includes Digital Video Compression, Digital image & video processing, Internet of Things (IoT) and Cloud Computing.

Dr. Sandeep Biswal (Senior Member IEEE) received his Ph.D. degree from National Institute of Technology Raipur, CG, India, in 2019. He received his Master degree in Electrical Engineering with specialization in Power System Engineering from VSSUT Burla, Odisha in 2013 and bachelor degree in Electrical Engineering in 2009 respectively. He has published many papers in Journals of international repute including IEEE Transaction in Power Delivery, IEEE Transaction in Industrial Informatics, EPSR, LEPES, AJSE, PCMPS, JEEEPS, IET GTD, and IET SMT. He has also published and presented many research papers in various national prestigious international conferences. He is also recipient of POSOCO PPSA 2019 in doctoral



category by IIT Delhi and PGCIL India. He is also an active reviewer of many reputed journals like IEEE Transaction of Power System, His main research interests are in Power System Relaying, Distribution System Protection, HVDC line Protection, Distribution System Planning, Power System Planning and Microgrid Protection. Dr. Biswal is a senior member IEEE and Life Member of ISTE (India). He is also associated with IRC IEEE and IEEE MP Section.

Dr. Pushpanjali Shadangi did her Ph.D. in the topic "Resilient DSTATCOM for power quality improvement" at OP Jindal University Raigarh, Chhattisgarh and completed her M. Tech degree in Power System, from Shree Ram Institute of Technology, Jabalpur, India and bachelor degree in Electrical & Electronics Engineering from OP Jindal Institute of Technology, under CSVTU Raigarh, India. She has more than 11 years of experience in both teaching and research and published more than 15 research articles in renowned journals like SCIE, Willey and springer book chapters and conferences. she has also published and presented many research papers in various national prestigious international conferences. Her



current research interests include power electronics controller design, Artificial Intelligence, fuzzy logic, Photovoltaic cells and its integration to grid. She is a IEEE member and life time member of international Association of Engineers (IAE), member of National institute of technical training & skill development.

Dr. Ajay Panday did his Ph.D. degree from O.P. Jindal University, Raigarh, CG, India, in 2025. He received his Master degree in Electrical Engineering with specialization in Power System Engineering from RGPV, Bhopal, MP in 2017 and bachelor degree in Electrical Engineering in 2013 respectively. He has published many papers in Journals of international repute including IEEE Transaction in Power Delivery, EPSR, IJEEEPS, Electrical Engineer, IJTRE, IJATSER. He has also published and presented many research papers in various national prestigious international conferences, His main research interests are Power System Protection., Renewable Energy, Transmission Line Protections



with the integration of renewable energy sources, Solar & EV. Dr. Ajay is a Life Member of ISTE (India), Member IEEE & Life Member IAENG.

Dr. Bhawana Deshmukh did her Ph.D. degree from Veer Surendra Sal University of Technology (VSSUT) Burla, Odisha, India. She received her Master's degree in Electrical Engineering with a specialization in Power Electronics Engineering from Raipur Institute of Technology, Raipur, Chhattisgarh and a bachelor's degree (AMIE) in Electrical Engineering from e Institution of Engineers (India), Kolkata. She is currently working as an Assistant Professor in Electrical Engineering Department at OP Jindal University Raigarh, Chhattisgarh. She has also published an SCIE paper in IJEPES Elsevier and presented many research papers at various national and prestigious international conferences. Her main research



interests are in Power System Relaying, Distribution System Protection and Planning and Micro-Grid Protection. Mrs Bhawana Deshmukh is a life member of the Associate member of Engineers(IEI) and also chartered Engineers.

Mr. Rakesh Patidar has completed his master degree in Instrumentation Engineering from Devi Ahilya Vishwavidyalaya, Indore India and bachelor degree in Electronics and Instrumentation from Institute of Engineering & Technology, Devi Ahilya Vishwavidyalaya, and Indore India. He has more than 15 years of experience in both teaching and research and published more than 14 international & national conferences. His current research interests include Microcontroller based system design, Internet of ings and embedded systems.



Mr. Shashikant has received B.Tech degree from Jawaharlal Nehru
Technological University, Kakinada in 2014 M.Tech degree from National
Institute of Technology, Raipur in 2018, and now pursuing Ph.D. degree from
National Institute of Technology, Raipur. He has research experience of
more than six years. He has pushed more than 10 research papers in
International Journal and conferences. His research ares Includes
Optimization, Machine Learning, Controller design, and Forecasting.



Mr. Rupesh Patel received his Master degree in Electrical Engineering with specialization in Industrial Electronics from National Institute of Technology Rourkela, Odisha, India, in 2014, and bachelor degree Electrical and Electronics Engineering in 2011 respectively. He was associated with Gov Prayas Residential School, Korba Chhattisgarh and before it as a lecturer with Shri Rawatpura Sarkar Institute of Technology Raipur. With more than 5 years of experience in teaching he is continuing his research work and between he achieved diploma degrees like



Dr. Shuvam Sahay did his Ph.D. in the topic "Performance Evaluation of Optimization Techniques on Grid Dispatch of Power Plants" from National Institute of Technology Jamshedpur and completed his M.Tech. in Power System from National Institute of Technology Jamshedpur. He also did PGDC in Hydro Power Engineering from NPTI HPTC Nangal. He has a total experience of 4.5 years including Industrial experience. He was previously working as an Assistant Professor in the Department of Electrical Engineering in Sarala Birla University, Ranchi. He also worked as project Engineer in GVK Hydel power plant under Hydro Magus Pvt. Ltd. He has published 4 International journals with 15 conferences including 6 book



chapters. He also awarded with a best research paper award by GH Raisoni College of Engineering, Nagpur in the year 2020. He was a member of IEEE. He reviewed research papers of reputed journals like (Soft Computing (Springer), Asian Journals of Control (Wiley), and System Assurance and management (Springer)). He attended and organized more than six Faculty development program held across the India.

Staff Members

Mr. K. P. Satish Completed diploma in electrical engineering from RKDF
University Bhopal in 2019. Christian college of engineering and technology, Bhilai
from 1999-2008 as a lab Assistant, later 2008 he joined as lab Assistant at OPJU
from 2008 to till date and 2024 as Lab Instructor at OPJU. He has more than 24
years of experience in which he served He is expertise in handling, maintaining,
and troubleshooting electrical machines, electrical drives, high voltage, and
Control System Lab



Mr. Kapol Nashine received a diploma in electronics and telecommunication engineering from Durg polytechnic college in 2004. He has experience in telecom as well as institutional experience of more than 18 years. From, 2004 to 2007 he worked at Bharti Airtel Ltd and was promoted to the level of technical officer, from 2007 to 2011 as a lab instructor at M P Christian college Bhilai, and then from 2011 to till date he is working as a lab Instructo in OP Jindal University, Raigarh. He has vital experience in the commissioning, maintenance, and conduction of microprocessors and microcontrollers, electronic devices and



circuits, linear integrated circuits, and digital electronics laboratories. Moreover, he has expertise in Multisim, Lab VIEW, SciLab, Keil, and Proteus, etc.

Jugal Kishor. received his M. Tech degree in Power System And Control, "An Enhanced GJO-Pattern Search Algorithm to Solve the Optimal Power Dispatch Problem" is the title of his M.Tech thesis, which he turned in to Kalinga University in Raipur, Chhattisgarh. He then graduated in 2022 with a B.Tech from Kirodimal Institute of Technology in Raigrah. In 2017, he earned his electrical engineering diploma from IBT College in Bhilai. Since 2019, he has been employed by OP Jindal University as a lab Instructor member in the electrical engineering department. In addition to fundamental electrical engineering, he has over 6



years of expertise in digital electronics, electrical circuits and network analysis, control systems, and power systems simulation labs. He knows a lot about simulation programs like PSCAD and MATLAB.

Mr. Pankaj Badek received his B.Tech degree in Electrical and Electronics Engineering from O. P. Jindal University, Raigarh. He has been working as a Lab Instructor at O. P. Jindal University since 2021, with over four years of experience in conducting and managing laboratory sessions in Power Electronics, Power Systems, Basic Electronics and Digital Electronics. He has hands-on experience with Arduino Uno for student projects and application in Power Electronics experiments. Mr. Badek is also proficient in simulation tools such as MATLAB and PSCAD.



EDITORIAL TEAM

Editors:



Dr. Pushpanjali Shadangi Assistant Professor, (Sr. Grade), OPJU



Mr. Jugal Kishor
Lab Instructor, Department of EE, OPJU, Raigarh.

Upcoming Event



2nd International Conference on Electrical and Electronics for Sustainable Innovations 2026 (ICEESI 2026)

O. P. JINDAL UNIVERSITY

O. P. Jindal Industrial Park, Punjipathra, Raigarh [C.G.] 496109

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